

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the below date:
Date: August 17, 2004 Name: Joseph F. Hetz

8/17/04

Date

Signature:

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In re	Appln. of:		-	ED STATES PA I.	TENT AN	Ď TRAD 	EMARK	OF	FICE	42101	_
Appli	n. No.:	10/774	1,758				Examin	er:			
Filed	:	Februa	ary 9, 2	2004			Art Un	it:	2818		
For:		•		itecture and Me Memory	thod for T	hree-					
Attor	ney Docke	t No:	1051	9-114							
P. O.	nissioner for F Box 1450 ndria, VA 22)			T	RANSM	ΊΤ	TAL		
Sir:											
Attacl	ned is/are:										
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				nt of \$ for a _					37 C.F.R.	§ 1.136(a).	
	A petition or	processi	ng fee ir	n an amount of \$	under 3	7 C.F.R. §	1.17(_).			
	An additiona	ıl filing fe	e has be	en calculated as sl	nown below	:					
						Sma	II Entity		Not a S	mall Entity	
	Claims Re After Ame	_		Highest No. Previously Paid For	Present Extra	Rate	Add'l Fee	ог	Rate	Add'l Fee	
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Indep.			Minus			x 43=			x \$86=		
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	A check in the	ne amoun	it of \$	is enclosed.							
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⊠	and any pat	ent appli e require	cation ped to en	rized to charge pays processing fees und processing fees und processing that this pap	der 37 CFR	§ 1.17 a	ssociated v	ith 1	his pape	r (including	any

Respectfully submitted,

Joseph F. Hetz (Reg. No. 41,070)

Joseph F. Hetz - Reg. No. 41,070

Name of Applicant, Assignee or
Registered Representative

Signature

Patent Our Case No. 10519-114

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicat	tion of:)		
	Crowley et al)		
)	Group Art Unit:	2818
Serial No.:	10/774,758)		
Filed:	February 9, 2004)		
)		
For:	System Architecture and Method)		
	for Three-Dimensional Memory)		

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Pursuant to the obligation under 37 C.F.R. § 1.56 and in conformance with 37 C.F.R. §§ 1.97-1.99, Applicants hereby submit documents A1-A24 listed on the attached form PTO-1449 for consideration by the Examiner. In accordance with the August 5, 2003 OG Notice, copies of the U.S. patents and patent application publications are not provided. Copies of the other documents are enclosed herewith. Applicants request that the Examiner review the entire disclosure of these documents and make them of record.

The filing of this Information Disclosure Statement does not constitute an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, Applicants reserve the right to contest that any of the information submitted herewith is prior art against the present application.

Dated: August 17, 2004

Respectfully submitted,

Joseph F. Hetz Reg. No. 41,070

Attorney for Applicants

BRINKS HOFER GILSON & LIONE P.O. Box 10395 Chicago, Illinois 60610 (312) 321-4719

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FORM PTO-1449	(5, 1,0)	A TRADER	SERIAL NO.		CASE NO.
	V D E Y			10/774,758	10519-114
LIST OF PATE	NTS AND PUBLICA	TIONS FOR	FILING DATE		GROUP ART UNIT
APPLICANT'S	INFORMATION DIS STATEMENT	CLOSURE	Febr	uary 9, 2004	2818
(use several sheets if	necessary)		APPLICANT(S):	Crowley et al.	

EXAMINER INITIAL		DOCUMENT NUMBER Number-Kind Code (if known)	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
	A1	6,034,882	3/7/2000	Johnson et al.		
	A2	6,420,215	7/16/2002	Knall et al.		
	A3	6,525,953	2/25/2003	Johnson et al.		
	A4	US 2004-0002186 A1	1/1/2004	Vyvoda et al.		
	A5	US 2003-0120858 A1	6/26/2003	March et al.		
	A6	US 2002-0081782 A1	6/27/2002	Cleeves		
	A7	US 2002-0136076 A1	9/26/2002	Kleveland et al.		
	A8	US 2002-0136045 A1	9/26/2002	Scheuerlein		
	A9	US 2003-0115514 A1	6/19/2003	llkbahar et al.		
	A10	US 2003-0115518 A1	6/19/2003	Kleveland et al.		
	A11	US 2002-0083390 A1	6/27/2002	Lee et al.		,
	A12	6,486,728	11/26/2002	Kleveland		
	A13	6,385,074	5/7/2002	Johnson et al.		
	A14	6,661,730	12/9/2003	Scheuerlein et al.		-
	A15	US 2002-0136047A1	9/26/2002	Scheuerlein		
	A16	6,504,753	1/7/2003	Scheuerlein et al.		
	A17	6,522,594	2/18/2003	Scheuerlein		
	A18	6,407,953	6/18/2002	Cleeves et al.		
	A19	6,515,904	2/4/2003	Moore et al.		
	A20	US 2003-0128581 A1	7/10/2003	Scheuerlein et al.		
	A21	US 2004-0027855 A1	2/12/2004	Kleveland et al.		

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER Number-Kind Code (if known)	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES OR NO

EXAMINER INITIAL	(Ir sym	OTHER ART – NON PATENT LITERATURE DOCUMENTS nclude name of author, title of the article (when appropriate), title of the item (book, magazine, journal, serial, aposium, catalog, etc.), date page(s), volume-issue number(s), publisher, city and/or country where published.
	A22	"An Improved Method for Making High-Density Nonvolatile Memory," U.S. Patent Application Serial No. 10/326,470, Inventors: S. Brad Herner, Maitreyee Mahajani, and Michael A. Vyvoda, Filing Date: 12/19/2002.

EXAMINER	DATE CONSIDERED	
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Page 2 of 2

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FORM PTO-1449	SERIAL NO.	CASE NO.
	10/774,758	10519-114
LIST OF PATENTS AND PUBLICATIONS FOR	FILING DATE	GROUP ART UNIT
APPLICANT'S INFORMATION DISCLOSURE STATEMENT	February 9, 2004	2818
(use several sheets if necessary)	APPLICANT(S): Crowley et al.	

EXAMINER INITIAL		OTHER ART – NON PATENT LITERATURE DOCUMENTS (Include name of author, title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date page(s), volume-issue number(s), publisher, city and/or country where published.						
	A23	"Current Sensing Method and Apparatus Particularly Useful for a Memory Array of Cells Having Diode-Like Characteristics," U.S. Patent Application Serial No. 09/896,468, Inventor. Roy E. Scheuerlein, Filing Date: 6/29/2001.						
	A24	"512Mb PROM with 8 Layers of Antifuse/Diode Cells," presented as ISSCC 2003/Session 16/Non-Volatile Memory/Paper 16.4 at the 2003 IEEE International Solid-State Circuits Conference (3 pages).						

EXAMINER	DATE CONSIDERED	
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